

Amendments to the Claims

The claims have been amended as follows. Underlines indicate insertions and ~~strikeouts~~ indicate deletions.

Claim 1 (currently amended): Ultrasonic pulser-receiver circuitry, for use with an ultrasonic transducer, the circuitry comprising:

a circuit board;

ultrasonic pulser circuitry supported by the circuit board and configured to be coupled to an ultrasonic transducer and to cause the ultrasonic transducer to emit an ultrasonic output pulse;

receiver circuitry supported by the circuit board, coupled to the pulser circuitry, including protection circuitry configured to protect against the ultrasonic pulse and including amplifier circuitry configured to amplify an echo, received back by the transducer, of the output pulse; and

a connector, proximate an end of the circuit board, configured to couple the ultrasonic transducer directly to the circuit board, to the pulser circuitry and receiver circuitry, wherein impedance mismatches that would result if the transducer was coupled to the circuit board via a cable can be avoided.

Claim 2 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 1 and, when in operation, having a rise time of less than 5 nanoseconds.

Claim 3 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 1 and, when in operation, having a rise time of less than 1 nanosecond.

Claim 4 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 1 and, when in operation with an ultrasonic transducer, having a front surface ring down of less than 60 nanoseconds.

Claim 5 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 1 and, when in operation with an ultrasonic transducer, having a front surface ring down of less than 40 nanoseconds.

Claim 6 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 1 and, when in operation with an ultrasonic transducer, having a transducer delay-line of less than 20 microseconds.

Claim 7 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 1 wherein no transducer delay-line is required.

Claim 8 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 1 and, when in operation with an ultrasonic transducer, having a focal length of about 19 microseconds.

Claim 9 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 1 and, when in operation with an ultrasonic transducer, having a depth of field, in time, of less than ± 32 nanoseconds.

Claim 10 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 1 and, when in operation with an ultrasonic transducer, having a depth of field, in time, of less than ± 2 microseconds.

Claim 11 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 1 and, when in operation with an ultrasonic transducer, having a depth of field, in inches, of less than 0.005 inch.

Claim 12 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 1 and, when in operation with an ultrasonic transducer, having a depth of field, in inches, of less than 0.136 inch.

Claim 13 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 1 wherein the circuit board has one side supporting at least a majority of the receiver circuitry and an opposite side supporting at least a majority of the pulser circuitry.

Claim 14 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 13 wherein at least a majority of the receiver circuitry is defined by components that are surface mounted onto the circuit board.

Claim 15 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 13 wherein at least a majority of the pulser circuitry is defined by components that are surface mounted onto the circuit board.

Claim 16 (original): Ultrasonic pulser-receiver circuitry, for use with an ultrasonic transducer, the circuitry comprising:

- a circuit board;

- ultrasonic pulser circuitry supported by the circuit board and configured to be coupled to an ultrasonic transducer and to cause the ultrasonic transducer to emit an ultrasonic output pulse, the pulser circuitry including an input configured to receive an input pulse from an external source, an input trigger amplifier coupled to the input, a trigger driver coupled to the trigger amplifier, a transistor coupled to the trigger amplifier, and circuitry, including a discharge capacitor and charging and discharging diodes, coupled to the transistor;

- receiver circuitry supported by the circuit board, coupled to the pulser circuitry, including protection circuitry configured to protect against the ultrasonic pulse and including amplifier circuitry configured to amplify an echo, received back by the transducer, of the output pulse; and

a connector configured to couple the ultrasonic transducer directly to the circuit board, to the pulser circuitry and receiver circuitry, wherein impedance mismatches that would result if the transducer was coupled to the circuit board via a cable can be avoided.

Claim 17 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 16 and, when in operation, having a rise time of less than 5 nanoseconds.

Claim 18 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 16 and, when in operation, having a rise time of less than 1 nanosecond.

Claim 19 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 16 and, when in operation with an ultrasonic transducer, having a front surface ring down of less than 60 nanoseconds.

Claim 20 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 16 and, when in operation with an ultrasonic transducer, having a front surface ring down of less than 40 nanoseconds.

Claim 21 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 16 and, when in operation with an ultrasonic transducer, having a transducer delay-line of less than 20 microseconds.

Claim 22 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 16 wherein no transducer delay-line is required.

Claim 23 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 16 and, when in operation with an ultrasonic transducer, having a focal length of about 19 microseconds.

Claim 24 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 16 and, when in operation with an ultrasonic transducer, having a depth of field, in time, of less than ± 32 nanoseconds.

Claim 25 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 16 and, when in operation with an ultrasonic transducer, having a depth of field, in time, of less than ± 2 microseconds.

Claim 26 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 16 and, when in operation with an ultrasonic transducer, having a depth of field, in inches, of less than 0.005 inch.

Claim 27 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 16 and, when in operation with an ultrasonic transducer, having a depth of field, in inches, of less than 0.136 inch.

Claim 28 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 16 wherein the circuit board has one side supporting at least a majority of the receiver circuitry and an opposite side supporting at least a majority of the pulser circuitry.

Claim 29 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 28 wherein at least a majority of the receiver circuitry is defined by components that are surface mounted onto the circuit board.

Claim 30 (original): Ultrasonic pulser-receiver circuitry in accordance with claim 28 wherein at least a majority of the pulser circuitry is defined by components that are surface mounted onto the circuit board.

Claim 31 (currently amended): An ultrasonic pulser-receiver comprising:
an ultrasonic transducer;
a circuit board;
ultrasonic pulser circuitry supported by the circuit board and coupled to the ultrasonic transducer to selectively cause the ultrasonic transducer to emit an ultrasonic output pulse, the pulser

circuitry including an input configured to receive an input pulse from an external computer, input trigger amplifier circuitry coupled to the input, a trigger driver coupled to the input trigger amplifier circuitry means, a high power transistor coupled to the trigger amplifier, and a discharge capacitor and charging and discharging diodes coupled to the transistor; and

receiver circuitry supported by the circuit board, coupled to the pulser circuitry, including protection circuitry configured to protect against the ultrasonic pulse and including amplifier circuitry configured to amplify an echo, received back by the transducer, of the output pulse, the ultrasonic pulser-receiver, in operation having a rise time of less than 1 nanosecond.

Claim 32 (original): An ultrasonic pulser-receiver in accordance with claim 31 and having, in operation, a front surface ring down of less than 60 nanoseconds.

Claim 33 (original): An ultrasonic pulser-receiver in accordance with claim 31 and having, in operation, a front surface ring down of less than 40 nanoseconds.

Claim 34 (original): An ultrasonic pulser-receiver in accordance with claim 32 and having, in operation, a transducer delay-line of less than 20 microseconds.

Claim 35 (original): An ultrasonic pulser-receiver in accordance with claim 33 wherein no transducer delay-line is required.

Claim 36 (original): An ultrasonic pulser-receiver in accordance with claim 34 and, in operation, having a focal length of about 19 microseconds.

Claim 37 (original): An ultrasonic pulser-receiver in accordance with claim 35 and, in operation, having a depth of field, in time, of less than ± 32 nanoseconds.

Claim 38 (original): An ultrasonic pulser-receiver in accordance with claim 36 and, in operation, having a depth of field, in time, of less than ± 2 microseconds.

Claim 39 (original): An ultrasonic pulser-receiver in accordance with claim 37 and, in operation, having a depth of field, in inches, of less than 0.005 inch.

Claim 40 (original): An ultrasonic pulser-receiver in accordance with claim 38 and, in operation, having a depth of field, in inches, of less than 0.136 inch.

Claim 41 (original): An ultrasonic pulser-receiver in accordance with claim 40 wherein the circuit board has one side supporting at least a majority of the receiver circuitry and an opposite side supporting at least a majority of the pulser circuitry.

Claim 42 (original): An ultrasonic pulser-receiver in accordance with claim 41 wherein at least a majority of the receiver circuitry is defined by components that are surface mounted onto the circuit board.

Claim 43 (original): An ultrasonic pulser-receiver in accordance with claim 42 wherein at least a majority of the pulser circuitry is defined by components that are surface mounted onto the circuit board.

Claim 44 (new): An ultrasonic pulser-receiver comprising:

an ultrasonic transducer;

a circuit board;

ultrasonic pulser circuitry supported by the circuit board and coupled to the ultrasonic transducer to selectively cause the ultrasonic transducer to emit an ultrasonic output pulse, the pulser circuitry including an input configured to receive an input pulse from an external computer, input trigger amplifier circuitry coupled to the input, a trigger driver coupled to the input trigger amplifier circuitry, a high power transistor coupled to the trigger amplifier, and a discharge capacitor and charging and discharging diodes coupled to the transistor;

receiver circuitry supported by the circuit board, coupled to the pulser circuitry, including protection circuitry configured to protect against the ultrasonic pulse and including amplifier circuitry configured to amplify an echo, received back by the transducer, of the output pulse;

a connector, proximate an end of the circuit board, configured to couple the ultrasonic transducer directly to the circuit board, to the pulser circuitry and to the receiver circuitry; and

a housing surrounding the circuit board and protecting the circuit board against water, after the transducer has been coupled to the connector;

the ultrasonic pulser-receiver having, in operation, a rise time of less than 1 nanosecond;

the ultrasonic pulser-receiver having, in operation, a front surface ring down of less than 60 nanoseconds;

the ultrasonic pulser-receiver having, in operation, a transducer delay-line of less than 20 microseconds; and

the ultrasonic pulser-receiver having, in operation, a depth of field, in inches, of less than 0.136 inch.